

2/83 H// Dkt. 2271/64016

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Shinichi YAMAURA et al.

Serial No.: 09/761,122

Group Art Unit: 2183

Date Filed: January 16, 2001

Examiner: David J. Huisman

For: PARALLEL PROCESSOR AND IMAGE PROCESSING APPARATUS ADAPTED

FOR NONLINEAR PROCESSING (as amended)

I hereby certify that this correspondence is being deposited this date with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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AMENDMENT

Sir:

This Amendment is submitted in response to the Office Action dated June 30, 2004 in connection with the above-identified application.

Amendment to the Title begins on page 2 of this paper.

Amendments to the Abstract begin on page 3 of this paper.

Amendments to the Specification begin on page 4 of this paper.

Amendments to the Claims are reflected in the **Listing of Claims** section which begins on page 11.

Amendments to the Drawings begin on page 18 of this paper.

Remarks begin on page 19 of this paper.

Exhibit A attached hereto includes corrected drawings for FIG. 10 and FIG. 15.

Amendment to the Title

Please amend the Title to the following:

--PARALLEL PROCESSOR AND IMAGE PROCESSING APPARATUS <u>ADAPTED</u>

<u>FOR NONLINEAR PROCESSING--</u>.